

UNLV AI-driven, Career Inspiring Experiential Program for Semiconductor Education (ACIES)

ACIES Training Workshops at UNLV (Jan. 23 ~ Jun. 11, 2027)

- Saturday Camp (01/23~03/13): Intro. to Electronic Sys.
- Saturday Camp (03/27~05/15): Circuit-Level Design
- Summer Camp (05/24~06/11): Semiconductor Manufacturing

Participants Will

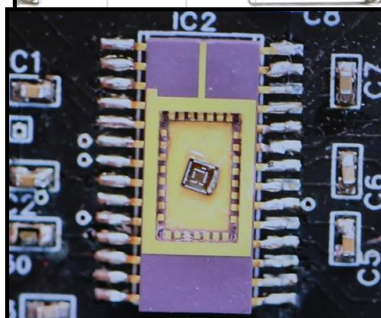
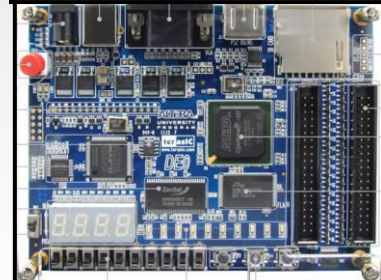
- Gain immersive learning experience in microelectronics and semiconductor fields
- Conduct hands-on projects in building circuits and manufacture chips
- Have opportunities to work as summer interns at local industry
- Be supported with \$1,000 stipend and hourly-paid wages for summer internship (up to 160 hrs, \$12.5/hr)
- Participate the education research on volunteer basis including surveys and audio-recorded interviews, each taking 10-15 minutes

Eligibility

- Students at Grade 10-12 who haven't participated in UNLV ACIES program before
- US citizen or permanent resident required

How to Apply

- Submit the [application form](#), transcripts, one-paragraph of statement of interests online
- Application deadline: **Monday, Nov. 16, 2026**
- Program contact: Dr. Mei Yang (Mei.Yang@unlv.edu), Dr. Shaoan Zhang (shaoan.zhang@unlv.edu)
- Website: <http://nsil.sites.unlv.edu/DTI.html>
- Scan the QR code to start your application



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